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Stabilized por	Stabilized porous, lectrically conductiv substrat_s
Patent Number:	☐ <u>US5939206</u>
Publication date:	1999-08-17
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Requested Patent:	☐ <u>JP10100410</u>
Application Number:	Application Number: US19960705916 19960829
Priority Number(s):	US19960705916 19960829
IPC Classification:	B32B27/06
EC Classification:	B32B15/00, B32B15/08
Equivalents:	
	Abstract
Disclosed is an appa member having elect subunits.	Disclosed is an apparatus which comprises at least one semiconductor chip mounted on a substrate, said substrate comprising a porous, electrically conductive member having electrophoretically deposited thereon a coating of a polymeric material. In one embodiment, the semiconductor chips are thermal ink jet printhead subunits.
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